

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/624,628
Filing Date July 21, 2003
Inventor.....Luan C. Tran
AssigneeMicron Technology, Inc.
Group Art Unit2812
ExaminerKennedy, Jennifer M.
Attorney's Docket No.MI22-2356
Title: Methods of Forming Semiconductor Constructions



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56.
Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: December 27, 2005

By: Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
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DEC 27 2005

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Application Number	10/624,628
Filing Date	07/21/2003
First Named Inventor	Luan C. Tran
Art Unit	2812
Examiner Name	J. Kennedy
Attorney Docket Number	MI22-2356

U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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EV 633202673